

ABSTRACT

A method and architecture for the extraction of data from or the insertion of data into Synchronous Optical Network (SONET) or Synchronous Digital Hierarchy (SDH) frames is disclosed. The method and architecture provides an interface that permits the time-multiplexed data streams being extracted or inserted to have variable data-rates and no fixed alignment with respect to each other. The extraction and insertion interface accommodates for variable POH data rates and alignment inconsistencies of POH bytes amongst different paths due to floating pointer positions. The interface operates at the lowest possible frequency that can still accommodate the minimum spacing between any two consecutive words of data for a given data stream. In the insertion case, the frequency of operation chosen also allows the pipelining of requests as well as the pipelining of the subsequent associated data in response. The implementation of the present invention minimizes the amount of storage and enables a relatively low required frequency of operation while maintaining a smooth clock. In addition, in the insertion case, the pipelined nature of the interface provides flexibility in meeting interface timing.